



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patentee: William J. Dally and John W. Poulton

Patent No.: US 6,952,431 B1

Issued: October 4, 2005

Title: CLOCK MULTIPLYING DELAY-LOCKED LOOP
FOR DATA COMMUNICATIONS

CERTIFICATE OF MAILING	
I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450	
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REQUEST FOR CERTIFICATE OF CORRECTION

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Certificate
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of Correction

Sir:

Pursuant to 35 U.S.C. § 254, we hereby request a Certificate of Correction be issued for the above-referenced U.S. Letters Patent for the following reasons:

Column 12, line 67, delete "rime" and insert --- time ---

Column 13, line 28, delete "communication" and insert --- communications ---

NOV 21 2005

Since the errors were made by the U.S. Patent and Trademark Office and not by Patentees or Patentee's Attorney/Agent, it is understood that there are no additional fees for the requested Certificate of Correction. A Certificate of Correction is enclosed herewith.

Respectfully submitted,

HAMILTON, BROOK, SMITH & REYNOLDS, P.C.

By 

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Dated: 11/8/5

NOV 21 2005

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,952,431 B1
DATED : October 4, 2005
INVENTOR(S) : William J. Dally and John W. Poulton

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby
corrected as shown below:

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MAILING ADDRESS OF SENDER:

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